IN THE SPECIFICATION

Please amend the specification as follows:

The paragraph beginning at page 1, after the title, is amended as follows:

This application is a continuation of U.S. Patent Application Serial No. 09/671,314, filed September 28, 2000, now issued as U.S. Patent No. 6,687,844, which is incorporated herein by reference.

The paragraph beginning at page 1, line 10 is amended as follows:

The embodiments of the invention relates relate to a device and method to correct for clock duty cycle skew in a processor.

The paragraph beginning at page 3, line 21 is amended as follows:

The foregoing and a better understanding of the embodiments of the present invention will become apparent from the following detailed description of exemplary embodiments and the claims when read in connection with the accompanying drawings, all forming a part of the disclosure of the embodiments of this invention. While the foregoing and following written and illustrated disclosure focuses on disclosing example embodiments of the invention, it should be clearly understood that the same is by way of illustration and example only, and the embodiments of the invention is are not limited thereto.

The paragraph beginning at page 4, line 11 is amended as follows:

Before beginning a detailed description of the subject invention, mention of the following is in order. When appropriate, like reference numerals and characters may be used to designate identical, corresponding or similar components in differing figure drawings. Further, in the detailed description to follow, exemplary sizes/models/values/ranges may be given, although the embodiments of the present invention is are not limited to the same. As a final note, well-known

components of computer networks may not be shown within the FIGs. for simplicity of illustration and discussion, and so as not to obscure the embodiments of the invention.

The paragraph beginning at page 9, line 24 is amended as follows:

The benefit resulting from the embodiments of the present invention is that clock duty cycle skew can be detected and corrected using a simple deterministic device and method, which can be built into each microprocessor chip. This eliminates the need for external equipment, and requires a minimal amount of space on the processor chip. Therefore, microprocessors may continue to increase in speed while still employing clocks that generate a certain amount of clock duty cycle skew.